

Working Group 10.5 (Design and Engineering of Electronic Systems)
Yearly report for the period August 2024 – July 2025
Written by Graziano Pravadelli

The WG 10.5 home page is located at: <https://cisdi.di.univr.it/wg10-5/>

1 - Members

Two new members have been added during the period:

- Dr. Leticia Bolzani Phoelp from IHP, Frankfurt, Germany
- Dr. Samuele Germiniani from University Marconi, Rome, IT

Then, there are in total 59 members and 12 honorary members.

The geographic distribution of the members is as follows:

- Europe: 36 members, 4 honorary members
- North America: 8 members, 5 honorary members
- Latin America: 3 members, 1 honorary member
- Asia and South Pacific 7 members, 2 honorary members
- Africa and Middle Est: 3 members

2 - Officers

Since October 2020 the WG chair is Prof. Graziano Pravadelli, University of Verona, Italy

Since March 2021 the WG vice chairs are:

- Prof. Ibrahim Elfadel, Masdar Institute, United Arab Emirates
- Prof. Ricardo Reis, Universidade Federal do Rio Grande do Sul, Brazil
- Dr. Victor Grimblatt, Synopsys, Chile
- Prof. Tiziana Margaria, University of Limerick, Ireland

Their roles of the WG chairs are as follows:

Name	Geographical area	Main expertise	Promotional role
Tiziana Margaria	Europe	Formal methods	Women enrolment
Ibrahim Elfadel	Africa, Asia and Far East	Cyber physical systems and IoT	Mentoring for VLSI-SOC organizers
Ricardo Reis	America	Physical design and testing	Networking with IFIP initiatives
Victor Grimblatt	Worldwide	EDA, electronic industry	Industrial liaison

3 - Meetings

The following business meetings took place in the period August 2024 to July 2025:

- October 6, 2024, hybrid, co-located with VLSI-SOC 24 in Tanger (Morocco), 12 attendees.

- March 31, 2025, hybrid, co-located with DATE 25 in Lyon (France), 22 attendees.

All meeting minutes are available at: <https://cisd.di.univr.it/wg10-5/meetings.html>

The following meetings are currently planned for the coming year:

- October 12, physical meeting in Puerto Varas (Chile) during VLSI-SOC 2025, with possibility of remotely connect for people that will not attend the conference.

4 - Sponsored Events

The main conference sponsorship activity of WG 10.5 is the annual IFIP/IEEE International Conference on Very Large Scale Integration, VLSI-SOC Conference.

The members of the Steering Committee are:

Matthew Guthaus (UC Santa Cruz, USA), Luis Miguel Silveira (INESC ID, PT), Chi-Ying Tsui (HKUST, Hong Kong, CN), Manfred Glesner (TU Darmstadt, DE), Ibrahim Elfadel (Masdar Institute of Science and Technology, UAE), Fatih Ugurdag (Ozyegin University, TR), Ricardo Reis (UFRGS, BR), Ian O'Connor (ECL, FR), Masahiro Fujita (The University of Tokyo, JP), Salvador Mir (TIMA, FR), Graziano Pravadelli (University of Verona, IT)

VLSI-SoC 2024

The 32th VLSI-SOC conference from October 6 to 9, 2024, was co-sponsored by IFIP, IEEE CEDA, IEEE CASS, TU Delft, Region Tanger, Cadence, CNRST Université Abdelmalk Essaadi.

Its venue was Tanger, Morocco in presence.

The Conference was organized and chaired by Said HAmidioui, TU Delft, NL, and Anass El Haddadi, ENSA, Morocco. The Program co-chairs were Dr. Elena Ioana Vatajelu, TIMA Grenoble, France, and Prof. Hussam Amrouch, TUM, Germany.

There were 3 keynotes, 6 regular sessions, 4 special sessions, 2 embedded tutorials, 2 panel sessions, 1 industrial session, 2 poster sessions, and 1 PhD forum. 60 persons participated to the embedded tutorials.

The conference had 102 valid submissions, with 79 regular papers, 3 industrial contributions, 5 proposals for special sessions, 2 embedded tutorials and 14 contributions to PhD and Student forum. Acceptance rate: 31% (25 oral presentations) + 21 posters.

Events co-sponsored by WG 10.5 without financial commitment

- **SBCCI 2025** - 38th Symposium on Integrated Circuits and Systems Design, August 25-29, 2024, Manaus, Brazil.
- **FDL 2025** – Forum on Specification and Design Languages, September 10-12, 2025, Schloss Rheinfels, St. Goar, Germany.

Planned Events

The following main sponsored events are already planned:

- **IFIP/IEEE VLSI-SoC 2025:** Puerto Varas, Chile, 12-15 October 2025, General Chairs: Prof. Dr. Victor Grimblatt (Synopsys, Chile), Pierre-Emmanuel Gaillardon (Univ. of Utah, US).
- **IFIP/IEEE VLSI-SoC 2026:** Limassol, Cyprus, October 2026, General Chairs: Prof. Tiziana Margaria and Prof. Ian O'Connor.

The following events are actually considered to be sponsored without any financial implication:

- **SBCCI 2025:** August, 2026.
- **FLD 2025:** Rome, Italy, September, 2026

5 – Other initiatives and discussions

The WG members discussed membership rules: after two years of inactivity, the WG chair will contact members via email to assess their interest and availability to continue participating in the WG.

The WG considered organizing an annual advanced school for young professionals focused on topics relevant to the WG. The first edition is scheduled for **September 1–6, 2025**, on **Favignana Island, Italy**. IFIP sponsorship was discussed but not finalized due to time constraints. Regardless, this inaugural event will serve as the foundation for establishing a recurring school with IFIP support starting in 2026.

The WG identified issues regarding the organization of funding to support PhD students and students from emerging countries attending **VLSI-SOC**, which should be clarified for future editions.